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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

JCS84 U.S. PTO  
09/342789  
06/29/99

**In re Application of:**

David J. Corisis

**Serial No.:** Not yet assigned

**Filed:** June 29, 1999

**For:** CHIP SCALE PACKAGE WITH  
HEAT SPREADER AND METHOD OF  
MANUFACTURE

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 3384.1US (97-564.1)

**NOTICE OF EXPRESS MAILING**

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Person making Deposit: Jared S. Turner

**INFORMATION DISCLOSURE STATEMENT**

Box PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

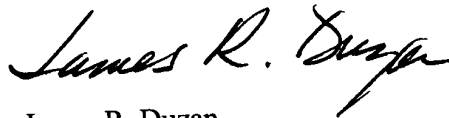
Sir:

The present application is a divisional of application Serial No. 09/028,134, filed February 23, 1998, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior application is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: June 29, 1999

JRD/jml

Enclosure: Form PTO-1449

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